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APPLICANTS

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** CONTINUING DATA ***** SDP
 This application is a DIV of 09/016,999 02/02/1998 PAT 6,596,572
 which is a DIV of 08/623,506 03/28/1996 PAT 5,767,529

** FOREIGN APPLICATIONS ***** SDP
 JAPAN 7-96266 03/28/1995

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <u>Monetta Lane SDP</u> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 30	INDEPENDENT CLAIMS 4
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TITLE
 Method of manufacturing a semiconductor method of manufacturing a thin-film transistor and thin-film transistor

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of
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